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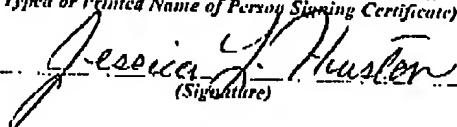
Applicant(s): HAINING YANG ET AL.

Docket No.

FIS920040194US1

Application No.
10/711,897Filing Date
10/12/2004Examiner
T. THOMASGroup Art Unit
2822Invention: METHOD AND STRUCTURE FOR IMPROVING CMOS DEVICE RELIABILITY USING
COMBINATIONS INSULATING MATERIALSI hereby certify that this Transmittal and Amendment
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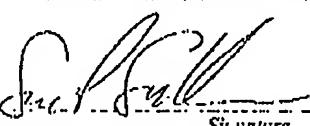
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AMENDMENT TRANSMITTAL LETTER (Large Entity) Applicant(s): HAINING YANG ET AL.				Docket No. FIS920040194US1	
Application No. 10/711,897	Filing Date 10/12/2004	Examiner T. THOMAS	Customer No. 29371	Group Art Unit 2822	Confirmation No. 5896
Invention: METHOD AND STRUCTURE FOR IMPROVING CMOS DEVICE RELIABILITY USING COMBINATIONS INSULATING MATERIALS					
<u>COMMISSIONER FOR PATENTS:</u>					
Transmitted herewith is an amendment in the above-identified application.					
The fee has been calculated and is transmitted as shown below.					
CLAIMS AS AMENDED					
	CLAIMS REMAINING AFTER AMENDMENT	HIGHEST # PREV. PAID FOR	NUMBER EXTRA CLAIMS PRESENT	RATE	ADDITIONAL FEE
TOTAL CLAIMS	23 -	24 =	0	x \$50.00	\$0.00
INDEP. CLAIMS	2 -	3 =	0	x \$200.00	\$0.00
Multiple Dependent Claims (check if applicable) <input type="checkbox"/>					
TOTAL ADDITIONAL FEE FOR THIS AMENDMENT \$0.00					
<p><input checked="" type="checkbox"/> No additional fee is required for amendment.</p> <p><input type="checkbox"/> Please charge Deposit Account No. in the amount of</p> <p><input type="checkbox"/> A check in the amount of to cover the filing fee is enclosed.</p> <p><input checked="" type="checkbox"/> The Director is hereby authorized to charge payment of the following fees associated with this communication or credit any overpayment to Deposit Account 09-0458</p> <p><input checked="" type="checkbox"/> Any additional filing fees required under 37 C.F.R. 1.16.</p> <p><input type="checkbox"/> Any patent application processing fees under 37 CFR 1.17.</p> <p><input type="checkbox"/> Payment by credit card. Form PTO-2038.</p>					
WARNING: Information on this form may become public. Credit card information should not be included on this form. Provide credit card information and authorization on PTO-2038.					
 <i>Signature</i> Dated: December 28, 2005					
Sean F. Sullivan Reg. No. 38,328 Cantor Colburn LLP 55 Griffin Road South Bloomfield, CT 06002 860-286-2929			I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to "Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450" [37 CFR 1.8(a)] on (Date)		
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	HAINING YANG, ET AL.)
Serial No.:	10/711,897) Group Art Unit: 2822
Filed:	October 12, 2004)
For:	METHOD AND STRUCTURE FOR IMPROVING CMOS DEVICE RELIABILITY USING COMBINATIONS INSULATING MATERIALS) Examiner: Thomas, T.) Confirmation No.: 5896

AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In response to the Office Action mailed November 16, 2005, please amend the Application as follows: